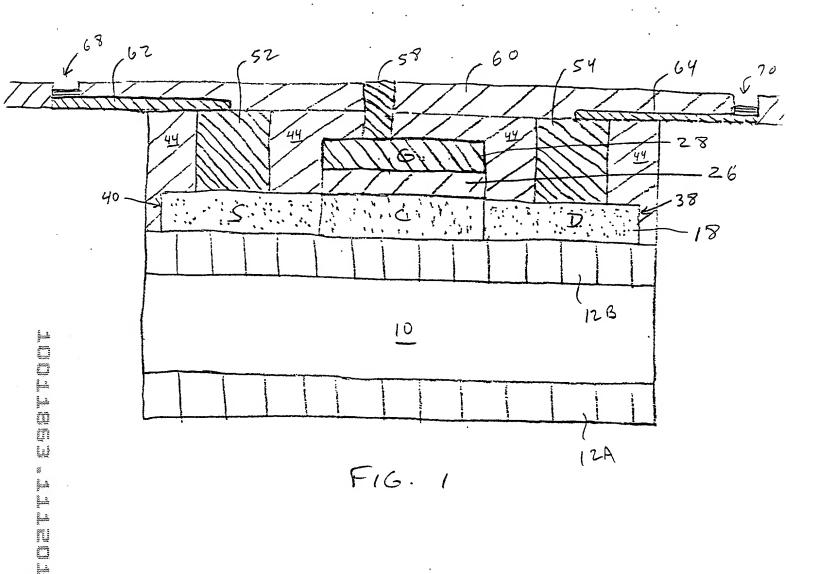
REDDY SMA-001.1D

Title:

INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME

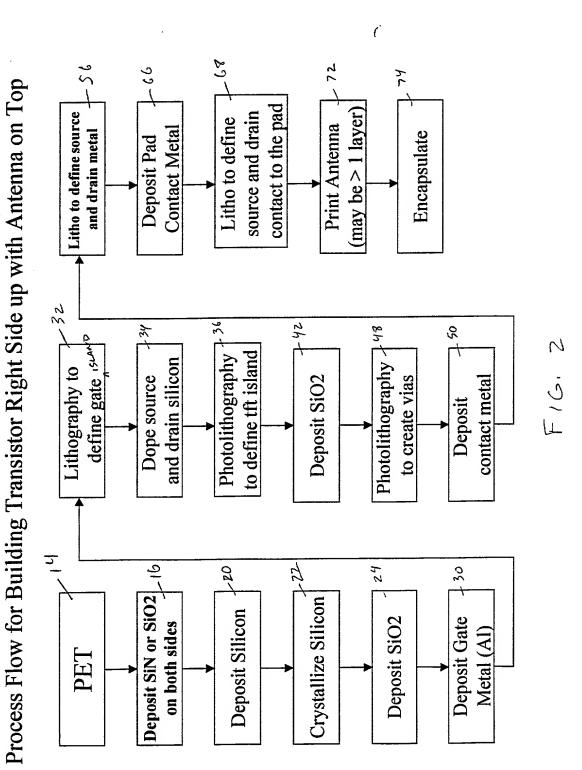


REDDY SMA-001.1D

Docket No.: SMA-00 Title: INEXPE

INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME

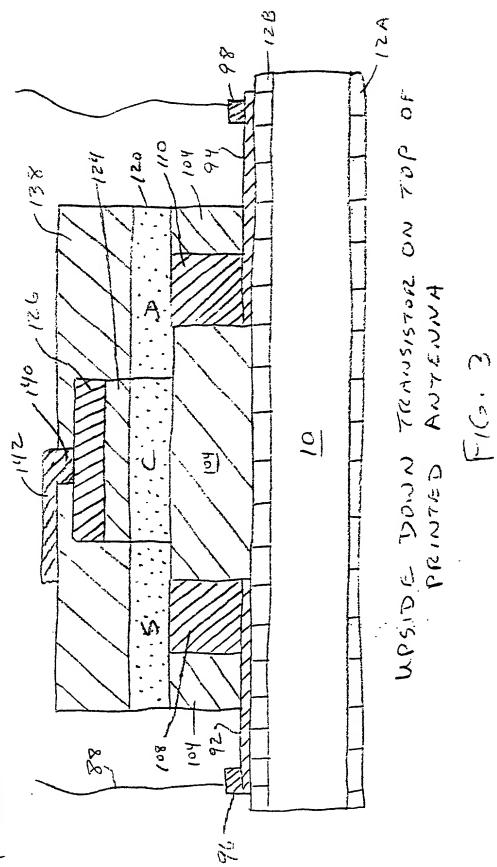




REDDY SMA-001.1D

Title:

INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND METHOD FOR MAKING SAME



ANTENNA

Inventor: REDDY Docket No.: SMA-001.1D

Title: INEXPENSIVE, RELIABLE, PLANAR

METHOD FOR MAKING SAME

RFID TAG STRUCTURE AND

821-134 Photolithography to define #(island and drain silicor Deposit Metal Dope source Litho silicon Encapsulate isolation Deposit **Si02** Process Flow for Building Transistor on Top of Antenna 901-127 2-Crystallize Silicon Deposit Metal (Al Deposit Silicon Deposit SiO2 Lithography to Litho metal to F10.4 define via fill vias 28 00 -107 % 3.1 9 Deposit SiN or SiO2 contact to the pad (may be > 1 layer)source and drain Litho to define Print Antenna Deposit SiO2 Contact Metal on both sides Deposit Pad PET

inventor: REDDY Docket No.: SMA-001.1D

Title: INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME

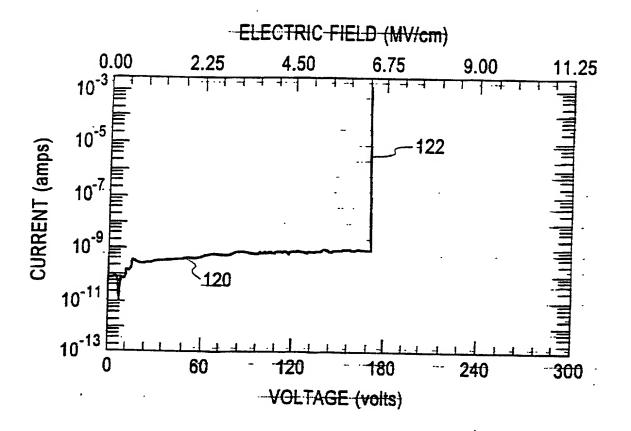


Fig. 5

Inventor: Docket No.: REDDY

SMA-001.1D
INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND Title:

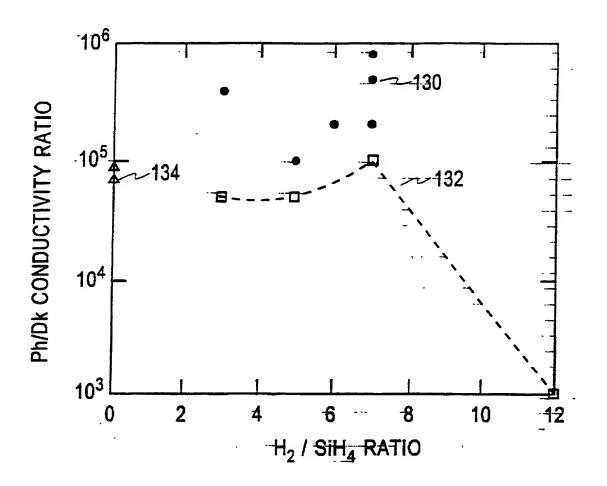


Fig. 6

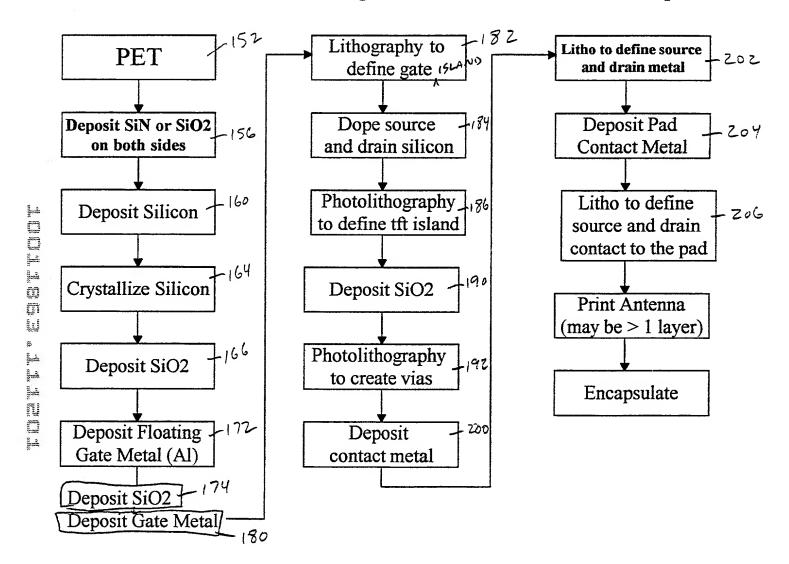
Inventor: REDDY Docket No.: SMA-001.1D

Title:

INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME

Process Flow for Building EEPROM with Antenna on Top

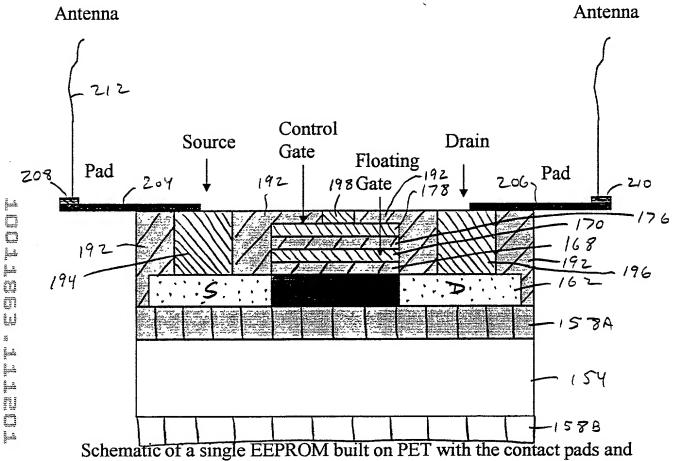


F16.7

Inventor: REDDY Docket No.: SMA-001.1D

Title: INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME



the antenna printed on top of the transistor; gate will be connected to the transistors (in actual devices multiple transistors and EEPROM will be connected to the contact pads)

F16. 8

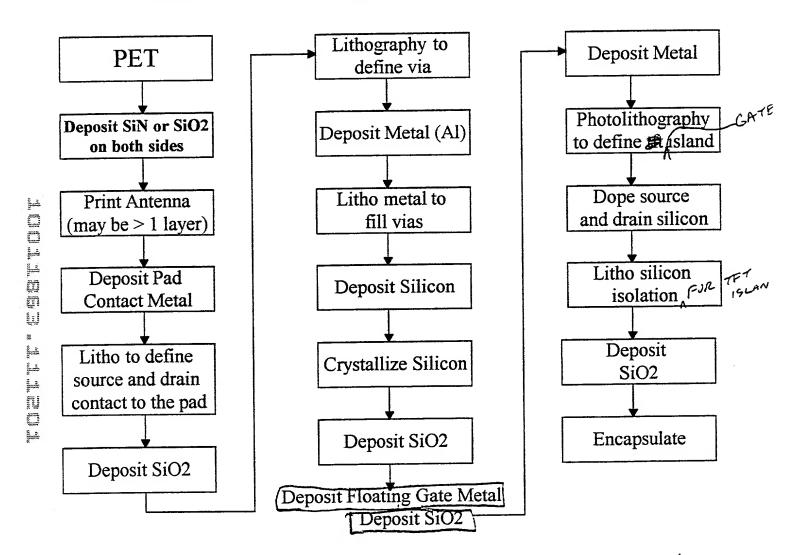
REDDY SMA-001.1D

Title:

INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME

Process Flow for Building EEPROM on Top of Antenna



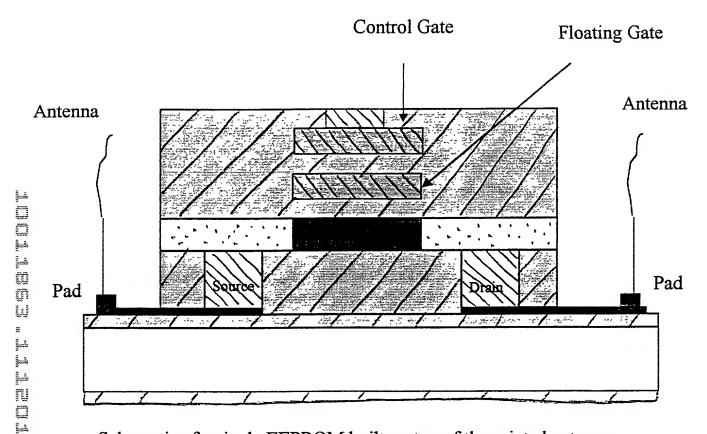
F16.9

Inventor: REDDY Docket No.: SMA-001.1D

Title:

INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND

METHOD FOR MAKING SAME



Schematic of a single EEPROM built on top of the printed antenna (in actual devices EEPROM and multiple transistors will be connected to the contact pads)

F16.10